	Application No.	Applicant(s)
	40742 200	CHAVES EIL HOLELISELL M
Notice of Allowability	10/712,288 Examiner	CHAVES FILHO, ELISEU M.  Art Unit
_		
	Frank Duong	2666
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>11/12/03</u> .		
2. The allowed claim(s) is/are <u>1-21</u> .		
3.		
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendr	ie

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## **DETAILED ACTION**

1. This Office Action is a response to communications dated 11/12/03. Claims 1-21 are pending in the application.

## Information Disclosure Statement

2. The information disclosure statement filed 01/09/06 complies with the provisions of 37 CFR 1.97, 1.98 and MPEP § 609. It has been considered and placed in the application file.

## Allowable Subject Matter

- 3. Claims 1-21 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: the disclosed invention relates to hardware unit within a DSP includes various circuits and components that allow spreading, complex scrambling, and complex correlation to be performed at the software level in a programmable processor at the speed levels required by third generation wireless communication systems. The prior art of record, considered individually or in combination, fails to fairly show or suggest, the claimed hardware unit, comprising, among other things, novel and unobvious elements for calculating a negative of an input signal; selecting either the input signal or the negative of input signal in response to a determination of a code register, structurally and functionally interconnected with other limitations in a manner as recited in claims 1-21.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Subramanian et al (USP 6,967,999).

DeHon et al (USP 5,956,518).

Singh et al, MorphoSys: An Integrated Reconfigurable System for Data-Parallel and Computation-Intensive Applications, IEEE, pages 465-481, May 2000.

Lee et al, Design and Implementation of the MorphoSys Reconfigurable

Computing Processor, Journal of VLSI Signal Processing Systems, pages 1-38, 2000.

- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Frank Duong whose telephone number is 571-272-
- 3164. The examiner can normally be reached on 7:00AM-3:30PM, Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chi H. Pham can be reached on 571-272-3179. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FRANK DUONG
PRIMARY EXAMINER

February 6, 2006